

## LISTA DE LUCRĂRI

**Prof. Univ. dr. ing. Mihai IORDACHE,**

### **I. TEZA (E) DE DOCTORAT (T)**

A1. M. Iordache , *Contributions to the nonlinear electrical system modeling in the transient behaviour*, Ph.D. dissertation, Politehnica University of Bucharest, Control Systems and Computer Science; Electrical Department, 1977, unpublished (coordinator: Prof.dr.ing. Marius PREDA).

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**Ca2.** Lucia Dumitriu, **M., Iordache**, *Modern Theory of Electrical Circuits - Vol. I - Theoretical Substantiation, Applications, Algorithms and Calculation Programs*, All Educational S.A Publishing House, Bucharest, 1998, ISBN 973 – 9337 – 99- 6 (508 pag.).

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**Ca6.** Lucia Dumitriu, **M. Iordache**, *Numerical Simulation of Analog Circuits by the SPICE Program*, Matrix ROM Publishing House, Bucharest, 2006, ISBN (10) 973 – 755 – 054 – 4, ISBN (13) 978 – 773 – 755 - 054 – 5 (133 pag.), [www.dragos.elth.pub.ro](http://www.dragos.elth.pub.ro).

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**Ca12.** Lucia Dumitriu, **Mihai Iordache**, *Sensitivity Computation Based on Auxiliary Circuits*, Chapter 9, in book *Design of Analog Circuits through Symbolic Analysis*, Mourad Fakhfakh, Esteban Tlelo-Cuautle and Francisco V Fernandez (Eds), Bentham Science Publishers, 2012, pp. 83-114, pp. 228-262 respectively (e-book), DOI: 10.2174/97816080509561120101, eISBN: 9781-60805-095-6, ISBN: 978-1-60805-425-1, 2012, Accession Number: **WOS:000425767300011**.

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### III. ALTE MATERIALE PUBLICATE (I,D)

#### Culegeri și îndrumare de laborator publicate în ediduri centrale

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**D2.** M. Preda și alții (**coautor**), "Bazele electrotehnicii - culegere de probleme" (reditată și completată), Ed. did. și pedagogică, București, 1983.

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#### IV. ARTICOLE / STUDII IN EXTENO PUBLICATE (R,V)

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## **V. BREVETE DE INVENTIE / INOVAȚII SAU CREAȚII ARTISTICE (B,A)**

**B - Brevete de inventie.**

**A - Inovații sau creații artistice.**

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